

IPW

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 10/565,006

Confirmation No.:

In re Application of:

Yasuyo SOGAWA, *et al.*

Group Art Unit:

Filed: January 18, 2006

For: SEMICONDUCTOR INTEGRATED CIRCUIT

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Customer Window
U.S. Patent & Trademark Office
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

Further to the Information Disclosure Statement filed on April 18, 2006 wherein Japanese Patent Application No. 52-67741 was cited, filed herewith is a statement provided by applicants' Japanese patent attorney.

Respectfully Submitted,


STEPTOE & JOHNSON LLP

January 8, 2007

RWP/lmw

Attorney Docket: 28951.1169

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Docket No.: 28951.1169

Applicant: Y. SOGAWA et al.

Application No.: 10/565,006

Filing Date: 01/18/2006

Examiner:

Group Art Unit:

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**

APR 18 2006

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS

[illegible]

**Examiner's
Signature**

/Latanya Crawford/

Date Considered

05/11/2007

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.